## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Applicant:

Visokay et al.

Filed:

Herewith

For:

METAL GATE MOS TRANSISTORS AND METHODS FOR MAKING THE SAME

## INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

1. Pursuant to 37 C.F.R. 1.97 and 1.98, and in compliance with 37 C.F.R. 1.56, the Office's attention is directed to the patents, pending applications, publications and other information listed on the attached PTO-1449. A copy of each listed document is enclosed except for: (a) pending applications or (b) those previously cited or submitted to the Office in the following application(s) upon which this application relies for an earlier filing date under 35 U.S.C. 120:

Serial No.:	
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Regarding any document, publication or other information for which a date is not given on the attached PTO-1449, Applicant(s) believe(s) the same may qualify as "prior" art to this application and should be treated accordingly, although Applicant(s) reserve(s) the right to contest the prior art status of any document, publication or information, should issue arise.

- 2. Regarding each listed document that is not in the English language, an English-language translation accompanies this Statement as indicated on the attached PTO-1449 or a concise explanation of the relevance of the document is set forth in the following document(s):
  - Copy of each English language version of a search report indicating the degree of relevance found by the foreign office of each document being submitted from the search report.
  - (b) \_\_\_\_ Attachment entitled "Concise Explanation of Relevance of Non-English Language Documents".
  - 3. Pursuant to 37 C.F.R. 1.97(b) this Statement is being filed (one must be checked):
    - (a) X Within 3 months of the filing date or date of entry into the National Stage.
    - Before the mailing date of a first Office Action on the merits. If this Statement is not filed before the mailing date of a first Office Action on the merits, the required certification is given below or, in the absence thereof, the Office is authorized to charge the required fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668 for consideration of this Statement.
    - Before the mailing date of a first Office Action on the merits after a first or second submission under 37 C.F.R. 1.129(a).

(d) After the period set forth in 37 C.F.R. 1.97(b) but before the mailing date of either a fina action or a notice of allowance.
(1) The required certification is given below, or
(2) Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(p) for consideration o this Statement, or
(3) Charge the fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668
(e) After the mailing date of either a final action or a notice of allowance, but before payment o the issue fee. Petition hereby is made for consideration of this Statement and the required certification is indicated below.
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4. Certification (if applicable)
(a) The undersigned hereby certifies that each item of information contained in this Statemen was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this Statement.
(b) The undersigned hereby certifies that no item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or to the undersigned's knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than 3 months prior to the filing of this Statement.
5. The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-0668.
Respectfully Submitted,
ESCHWEILER & ASSOCIATES, LLC
By Thomas G. Eschweiler
Registration No. 36,981 National City Bank Building 629 Euclid Avenue, Suite 1210 Cleveland, Ohio 44114 (216) 502-0600
CERTIFICATION UNDER 37 CFR 1.10
I hereby certify that this paper, and the documents referred to as attached or enclosed, are being deposited with the United States Postal Service on the date set forth below in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR 1.10 with the below indicated mailing label number, address do to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
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STATEMENT BY APPLICANT				First Named Inventor	Visokay et al.		
				Group Art Unit			
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Sheet	1	of	2	Attorney Docket No.	TI-35942		

	U.S. PATENT DOCUMENTS							
Exam. Initials*	Cite No.1	Number		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	AA	4,628,588		McDavid _	12/16/1986	Entire Document		
	АВ	4,641,417		McDavid	02/10/1987	Entire Document		
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	AN	6,617,624	B2	Powell	09/09/2003	Entire Document		

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Foreign Patent Document			Name of Patentee	Date of Pub.		. 9:0		
Exam. Initials*	Cite No. <sup>1</sup>	Office <sup>3</sup>	Number⁴	Kind Code <sup>2</sup> (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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"An Investigation of Molybdenum Gate for Submicrometer CMOS", ROBERT F. KWASNICK, EDMUND B, KAMINSKY, PAUL A. FRANK, GERHARD A FRANZ, KENNETH J. POLASKO, RICHARD J IAIA and THOMAS B. GORDZYA, IEEE Transactions on Electron Devices, Vol. 35, No. 9, September, 1988, pp. 1432-1438.							
	СВ	"Silicon Processing for the VLSI Era, Volume 2: Process Integration", STANLEY WOLF, Ph.D., Copyright, 1990 by Lattice Press, 7 pgs.					
"Electrical Characteristics of TiB <sub>2</sub> for ULSI Applications", CHANG SIK CHOI, QINGFENG WANG, CARLTON M. OSBURN, GARY A. RUGGLES and AYAN S. SHAH, IEEE Transactions on Electron Devices, Vol. 39, No. 10, October, 1992, pp. 2341-2345.							
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				First Named Inventor	Visokay et al.			
	STATEMENT	DI AIT LICAN		Group Art Unit				
	(use as many s	heets as necessary)		Examiner Name				
Sheet	2	of	2	Attorney Docket No.	TI-35942			

CD	"FinFET Process Refinements for Improved Mobility and Gate Work Function Engineering", YANG-KYU CHOI, LELAND CHANG, PUSHKAR RANADE, JEONG-SOO LEE, DAEWON HA, SRIRAM BALASUBRAMANIAN, ADITYA AGARWAL, MIKE AMEEN TSU-JAE KING and JEFFREY BOKOR, IEEE, 2002, 4 pgs.	
CE	"MO2N/Mo GAFF MOSFETs", MANJIN J. KIM and DALE M. BROWN, IEEE, 1982, 4 pgs.	
CF	"Application of MoSi <sub>2</sub> to the Double-Level Interconnections of I <sup>2</sup> L Circuits", YOSHITAKA SASAKI, OSAMU OZAWA and SHUICHI KAMEYAMA, IEEE Transactions on Electron Devices, Vol. ED-27, No. 8, August, 1980, 5 pgs.	
CG	"Work Function Controlled Silicide Technology for Submicron CMOS", MASAKAZU KAKUMU and KAZUHIKO HASHIMOTO, No source or date available, 2 pgs.	
СН	"Refractory Metal Silicide/N <sup>+</sup> Polysilicon in CMOS/SOS", B. C. LEUNG and J.S. MAA, No source or date available, 4 pgs.	
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